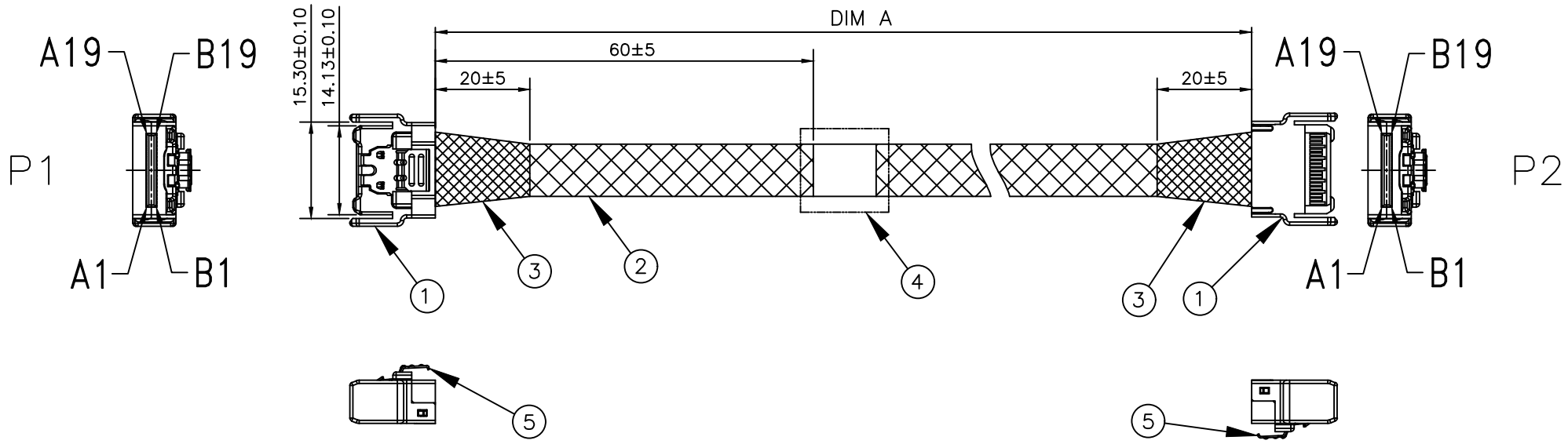
NOTES:

1. ALL CABLE 100% OPEN, SHORT, MISS WIRE & TRANSIENT FAULT TEST.
2. CONDUCTIVE RESISTANCE: 2 OHM MAX.
3. INSULATION RESISTANCE: 10M OHM MIN WITH 200Vac AT 10ms.
4. HIGH FREQUENCY TEST MUST COMPLY WITH PCIE GEN5 REQUIREMENT.
   CONNECTOR IMPEDANCE: X OHM ±15 OHM
   CABLE IMPEDANCE: X OHM ±10 OHM
5. ALL MATERIAL AND PROCESS MUST BE MEET THE REQUIREMENT OF RoHS2.0
6. PART NUMBER:

320-10496-XX
- LENGTH:1=0.5M,2=1.0M
- IMPEDANCE:0=85OHM,1=92OHM,2=100OHM

PROPOSAL DRAWING



320-10496-X1	PART NUMBER
0.5M±10mm	DIM A
320-10496-X2	PART NUMBER
1M±10mm	DIM A

ITEM	DESCRIPTION		
	MCIO (x4 STR to x4 STR)	Q'TY	UNIT
1	MCIO 4X STRAIGHT TYPE	2	PCS
2	EXPANDO TUBE.	A/R	MM
3	ACETATE TAPE COLOR: BLACK.	A/R	MM
4	LABEL.	1	PCS
5	LATCH.	3	PCS

QUALITY SYMBOLS
MAJOR
CRITICAL

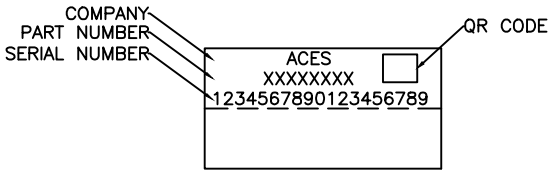
GENERAL TOLERANCES (UNLESS SPECIFIED)
X. ±0.15
.X ±0.10
.XX ±0.05
.XXX ±0.02
ANGLES ±1°

DRAWN BY: Wu, Chia Yi	DATE: 21'/10/28	ACES ELECTRONICS	
CHECKED BY: Chan, Ching Jung	DATE: 21'/10/28	TITLE: MCIO 38P STR To MCIO 38P STR	
APPROVED BY: Chan, Ching Jung	DATE: 21'/10/28		
UNITS: mm		SIZE: A4	RFQ NO.: N/A
SCALE: 1 : 1	SHEET NO.: 1 OF 2	REV: 1	DWG NO.: 320-10496-XX

P1 ⟷ P2

P1		P2	
B19	GROUND	A19	GROUND
B18	Tx5−	A18	Rx5−
B17	Tx5+	A17	Rx5+
B16	GROUND	A16	GROUND
B15	Tx4−	A15	Rx4−
B14	Tx4+	A14	Rx4+
B13	GROUND	A13	GROUND
B12	Tx3−	A12	Rx3−
B11	Tx3+	A11	Rx3+
B10	GROUND	A10	GROUND
B9	Tx2−	A9	Rx2−
B8	Tx2+	A8	Rx2+
B7	GROUND	A7	GROUND
B6	Tx1−	A6	Rx1−
B5	Tx1+	A5	Rx1+
B4	GROUND	A4	GROUND
B3	Tx0−	A3	Rx0−
B2	Tx0+	A2	Rx0+
B1	GROUND	A1	GROUND
A19	GROUND	B19	GROUND
A18	Rx5−	B18	Tx5−
A17	Rx5+	B17	Tx5+
A16	GROUND	B16	GROUND
A15	Rx4−	B15	Tx4−
A14	Rx4+	B14	Tx4+
A13	GROUND	B13	GROUND
A12	Rx3−	B12	Tx3−
A11	Rx3+	B11	Tx3+
A10	GROUND	B10	GROUND
A9	Rx2−	B9	Tx2−
A8	Rx2+	B8	Tx2+
A7	GROUND	B7	GROUND
A6	Rx1−	B6	Tx1−
A5	Rx1+	B5	Tx1+
A4	GROUND	B4	GROUND
A3	Rx0−	B3	Tx0−
A2	Rx0+	B2	Tx0+
A1	GROUND	B1	GROUND

PROPOSAL DRAWING



LABEL

HIGH−SPEED SERIAL DIFFERENTIAL PAIRS

SIGNAL RETURN

SIDEBAND SIGNAL

QUALITY SYMBOLS MAJOR CRITICAL	DRAWN BY: Wu, Chia Yi — DATE: 21'/10/28 CHECKED BY: Chan, Ching Jung — DATE: 21'/10/28 APPROVED BY: Chan, Ching Jung — DATE: 21'/10/28	ACES ELECTRONICS	
GENERAL TOLERANCES (UNLESS SPECIFIED) X. ±0.15 .X ±0.10 .XX ±0.05 .XXX ±0.02 ANGLES ±1°		TITLE: MCIO 38P STR To MCIO 38P STR	
	UNITS: mm	SIZE: A4	RFQ NO.: N/A
	SCALE: 1 : 1 — SHEET NO.: 2 OF 2	REV: 1	DWG NO.: 320−10496−XX